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LIST OF PRIOR ART CITED BY APPLICANT

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L. Richard CarleyFILING DATE
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U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	IF APPROPRIATE
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
EL	AL	EP 0 624 900 A2	04/18/94	European			X	
	AM							
	AO							
	AP							

OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)

EL	AR		Gianchandani, Y.B., Kim, H., Shinn, M., Ha, B., Lee, B., Najafi, K. and Song, C., "A MEMS-First Fabrication Process for Integrating CMOS Circuits with Polysilicon Microstructures", 1998 IEEE, pgs. 257-262
EL	AS		Kniffin, Margaret L., Shah, Mahesh, "Packaging for Silicon Micromachined Accelerometers", The International Society for Hybrid Microelectronics, Volume 19, Number 1, First Quarter 1996, pgs. 75-86
	AT		

EXAMINER

Eric Kiehn

DATE CONSIDERED

9/27/02

* EXAMINER Initial if reference considered, whether or not citation is in conformance with PEP 609. Draw line through question if not in conformance and not considered. Include copy of this form with next communication to applicant.